

THE BIRTHPLACE OF SILICON VALLEY

AUGUST 15, 2018

DESCRIPTION

Celebrating the Birthplace of Silicon Valley

Including the unveiling of an IEEE plaque by IEEE President James Jeffries

IEEE Santa Clara Valley Section

- IEEE Historical Milestones Santa Clara Valley Section

Registration Link: <https://www.eventbrite.com/e/celebrating-the-birthplace-of-silicon-valley-tickets-47680890828>

PROGRAM

3:00 PM Presentations

4:00 PM Unveiling of the IEEE “Birthplace” plaque

4:30 PM Short tours of the site, including two IEEE plaques and artist’s metal sculptures, light refreshments

Abstract:

The scientists and engineers who worked at 391 San Antonio Road in Mountain View, California, laid the technological and cultural foundations for today’s Silicon Valley. Employing some of the most brilliant young minds in the business, Shockley Semiconductor Laboratory produced Northern California’s first silicon transistor prototypes in the mid-1950s. However, due to William Shockley’s difficult management style, eight Shockley employees—including Gordon Moore, Robert Noyce, Julius Blank, Victor Grinich, Jean Hoerni, Eugene Kleiner, Jay Last, and Sheldon Roberts—resigned in September 1957 and founded Fairchild Semiconductor Corporation. Fairchild was the seedling from which companies valued at over \$2 trillion have grown and the source of the integrated circuit “computer chip” that has revolutionized our world.

Now, nearly 70 years later, the site of Shockley Labs, already an IEEE Historical Milestone, is being formally recognized by the IEEE and the City of Mountain View for its historical significance in a special dedication ceremony on August 15. Thanks to the efforts of many, especially developer Merlone Geier Partners, newly commissioned public sculptures—in the likeness of two early semiconductor devices and a mammoth silicon crystal monument that symbolize the work to come out of the lab—now permanently mark the site, along with various plaques that describe and commemorate the site’s history.

The event's featured speaker is Professor James F. Gibbons, former dean of engineering at Stanford University. Prof Gibbons' first task at Stanford in 1957 was to work with Shockley and his team to transfer their knowledge of silicon fabrication to Stanford, which could in turn train future engineers for the coming boom in the semiconductor industry. He will share his personal experiences and memories of those early days.

Join early semiconductor pioneers, the president of the IEEE, and local officials on August 15 to commemorate this legendary Silicon Valley landmark. Guests are invited to enjoy a series of presentations and exhibits and view the stunning sculptures and plaques.

Bio of Featured Speaker James F. Gibbons:

Jim joined the Stanford faculty in 1957 and was appointed professor of electrical engineering in 1964 and dean of the School of Engineering in 1984. In 1983 Jim was named Reid Weaver Dennis Professor of Electrical Engineering and in 1984 the Frederick Emmons Terman Dean of the School of Engineering. Jim served as dean from September 1984 to June 1996.

Jim has been an advisor and board member of public companies Cisco Systems, El Paso Energy, Lockheed Martin, and Raychem.

Jim's research accomplishments are in the area of semiconductor device analysis, process physics technology, and solar energy. Jim has written or co-written several books and nearly 300 papers. He was named Special Counsel to the Stanford University President and Provost for Industry Relations in 1966. Jim is credited with developing the Tutored Video Instruction Technique, a model for education of engineers, with colleagues from Stanford in 1972. In recent years, Jim applied that model to improve productivity and quality in children's education, particularly for children of migrant farm workers and at-risk teens.

Jim has received many awards and was elected a member of the National Academy of Engineering, the National Academy of Sciences, and the American Academy of Arts and Sciences. In 1997, Jim was inducted into the Santa Clara County Business Hall of Fame and was one of three chosen for the Silicon Valley Engineering Hall of Fame.

Jim received a BS (1953) degree from Northwestern University and a PhD (1956) from Stanford University.

Event Details:

When:

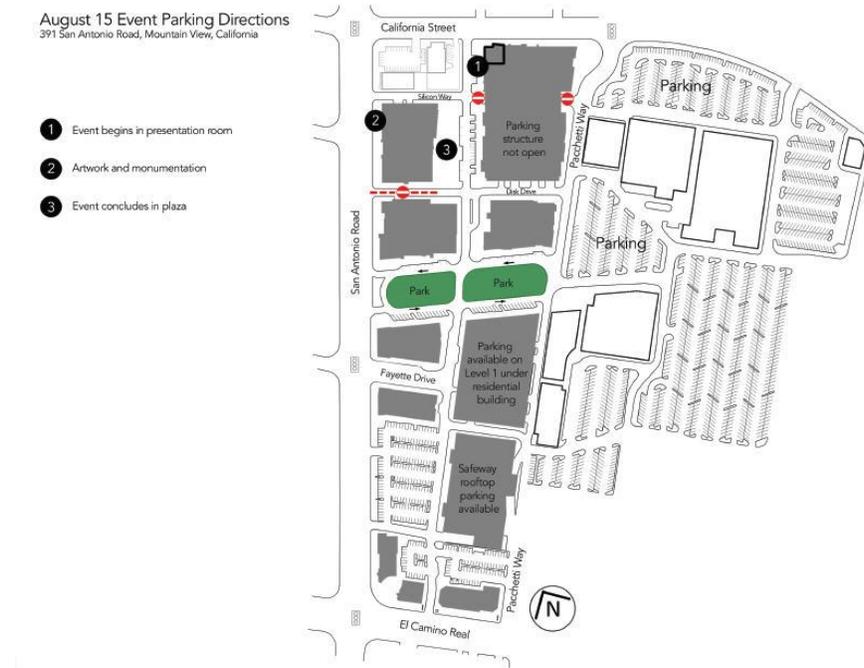
Wednesday, August 15, 2018

3:00 PM–5:00 PM

Venue:

391 San Antonio Rd., Mountain View, CA (Phase II of San Antonio Village)

Parking free at the parking garage at the site. Email Liz Munger, lmunger@merlonegeier.com for details of parking and the building location information.



Admission Fee:

Free

Registration required

Limited to 300 people

You do not need to be an IEEE member to attend.