

October Meeting: Challenges in Hardware Logic Verification

Co-Sponsored with Mid-Hudson Section Women in Engineering

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DATE: Thursday, October 25th, 2001

TIME: 7:00 – 9:00 PM

LOCATION: Casperkill Country Club
South Road
Poughkeepsie, NY 12601

SPEAKER: Bruce Wile
IBM Server Group Verification Lead

ABSTRACT: The computer industry recognizes logic verification as the main bottleneck in product time-to-market for hardware components, SOCs, and server systems. Long in the critical path of complex microprocessor and high-end server processes, logic verification has now come to the forefront of the entire industry due to the growing complexity of all IC components. This talk will address the challenges of verifying complex hardware designs and discuss the various methods used in the process. It will focus on problem areas faced in the industry and examine many of the types of “bugs” that make logic verification such a difficult task. The lecture will conclude with a look to the future and the types of methods that are needed to keep up with the increase in logic complexity.

SPEAKER BIO: Bruce Wile is currently IBM's Server Group verification lead. In this role, he's responsible for driving technology and methodology execution, as well as personnel initiatives such as education. Bruce joined IBM in 1985 out of Penn State University. He worked in verification from the start, originally verifying the S/390 ES/9000 series' storage controller unit. He's had many roles in verification, from team leader to manager. Bruce has several patents and has published papers in the IBM Journal of Research and Development along with multiple conferences. Prior to his current role, Bruce spent 1 year out of verification in 1999 as the technical assistant to the VP of S/390 hardware.