IEEE Bombay Section

Presents

A half-day Workshop on

The Art of Analog Design

by

Robert A. Pease

National Semiconductor, USA

May 19, 2004 2 - 6 pm

Seminar Hall, KReSIT, IIT Bombay

Co-sponsored by

National Semiconductor

IIT Bombay

For the first time in IEEE Bombay Section history, this workshop will also be **simultaneously available through a 2-way broadcast**, in association with the Distance Education Program of **Kanwal Rekhi School of Information Technology**, IIT Bombay, to the following **remote centers**:

1. Mahatma Gandhi Mission's College of Engineering and Technology, Hingoli Road, Near Airport, **Nanded** - 431 605 [mgmcen@vsnl.com]

2. Center for Development of Advanced Computing, Rain Tree Marg, Near Bharati Vidyapeeth, Opp. Kharghar Railway Station, Sector - 7, CBD Belapur, **Navi Mumbai** - 400 614 [dep@ncst.ernet.in,crsadmin@ncst.ernet.in]

3. Shri G.S. Institute of Technology and Science (SGSITS), 23 Park Road, **Indore** - 452 003, MP [dep@sgsits.ac.in]

4. Sinhgad Technical Education Society (STES), 44/1, Vadgoan (Bk), off Sinhgad Road, **Pune** - 411 401 [it_dep@sinhgad.edu]

5. Visvesvaraya National Institute of Technology [VNIT] (formerly VRCE), **Nagpur** - 440 011 [dep@vnitnagpur.ac.in]

6. Goa University Taleigoa, **Goa** - 403 206 [deprc@unigoa.ernet.in]

This workshop will be conducted by Bob Pease, the legendary analog circuit designer from National Semiconductor.

The workshop will have technical, but highly practical presentations containing a blend of advanced analog design concepts, tutorial review of basic principles, and "hands-on" application examples. It is intended for design engineers, technicians and students. Brush up on a full palette of topics, including:

- Precision & High-Speed Op Amps
- Low-Voltage & Low-Power Op Amps
- Precision Current Regulators
- Audio Amplifiers
- Analog-to-Digital Converters
- Temperature Sensors
- Current and Power Regulators
- Precision Resistor Measurements

Additionally, the topics of:
Voltage and Power Regulators, and WEBENCH™ Power-Supply Design Systems will be presented by notable experts Wanda C. Garrett and Jonathan E. Cronk, by a video recording.

Biography of the Speaker:
Robert A. Pease (featured as the analog artist in the picture) graduated from MIT in 1961 with a BSEE degree. He was employed at Philbrick Researches up to 1975 and designed many Operational Amplifiers and Analog Computing Modules. Pease joined National Semiconductor in 1976. He has designed about 20 analog ICs including power regulators, voltage references, and temperature sensors. He has written about 65 magazine articles and holds about 21 US patents. Pease is the self-declared Czar of Bandgaps since 1986. He enjoys hiking, and trekking in Nepal. His position at NSC is Staff Scientist. He is a Senior Member of the IEEE. Pease was inducted into the Electronics Engineering Hall Of Fame in October 2002.

Pease has written the definitive book, *Troubleshooting Analog Circuits* (May 1991), now in its 16th printing. It has been translated into French, German, Dutch, and Russian. Pease is a columnist in Electronic Design magazine, with over 200 columns published. The column, PEASE PORRIDGE, covers a wide range of technical topics. Many of Pease's recent columns are posted at:
http://www.elecdesign.com/Index.cfm?Action=Pease

For details on the Distance Education Program of KReSIT, see http://www.dep.iitb.ac.in/rc.html

Attendance in Mumbai and remote centers is free for IEEE members and student members, on production of the IEEE membership card