



## 2006 IEEE/EDS Workshop on Microelectronics and Electron Devices (WMED-2006)

*Fourth Regional Meeting, Boise Center on the Grove, Boise, Idaho, April 14, 2006  
IEEE Electron Devices Society*

### **WMED 2006 Announces Tutorials and an Extended March 17th Paper Submission**

The WMED organizers would like to announce that the Final Paper submission due date has been extended to **Friday, March 17th**. The WMED organizing committee decided that it needed more paper submissions for an improved contributing sessions and poster sessions. For these reasons, the active participation of industry and academic authors within the Pacific Northwest region is strongly encouraged. With this final extension of the due date, the authors of the existing submissions can edit and modify their work up to the deadline.

We continue to solicit 2-page paper in the IEEE Electron Device Letters format. Remember that the published material can be a "work in a progress" status. More information is found on the WMED website in the following link:

<http://www.ewh.ieee.org/r6/boise/wmed2006/Welcome.htm>

The WMED 2006 Tutorial and Invited Speakers are listed below:

KEYNOTE SPEAKER	
Al Fazio; Director, Memory Technology Development, Intel Corp.	<i>"Future Directions of Non-Volatile Memory Technologies"</i>
TUTORIAL SPEAKERS	
Christopher L. Henderson; President, Semitracks Inc.	<i>"Current Reliability and Failure Analysis Issues for High Volume Memory Products"</i>
Nozad Karim; VP of Application Engineering and Characterization, Amkor Technology Inc.	<i>"3D Memory Packaging Technologies &amp; Trends"</i>
INVITED SPEAKERS	
Dennis Hess; William W. LaRoche, Jr. Chair, School of Chemical & Biomolecular Engineering, Georgia Tech	<i>"Preserving Low-k Dielectrics During Photoresist and Etch Residue Removal"</i>
Karl Strauss; Senior Engineer, Flight Avionics Systems, Jet Propulsion Laboratory	<i>"Lest We Never Forget: A Historical Perspective of Space Data Recorders; A Prediction for the Future"</i>
Neil Goldman; Professor, Department of Electrical Engineering; Univ. Of Maryland at College Park	<i>"Physics Based Modeling and Design of Chips, Devices, and Nanostructures: Present and Future"</i>

Best Regards,

Fernando Gonzalez  
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Process R&D Thin Films Department  
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IEEE/EDS WMED Publicity Chair

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