

November 2005



EMC FOCUS

Official Newsletter of the Los Angeles Chapter

Meeting Details

- **Date:**
Wednesday,
November 2nd
- **Time:**
6:00 P.M. Social
7:00 P.M. Presentation
- **Location:**
WEMS Electronics
4650 W. Rosecrans Ave.
Hawthorne, CA 90250
(310)-644.0251
- **RSVP:**
By Tuesday Noon To
Kanaiya Mahendra
(310)-336-1647
- **Cost:**
FREE With RSVP
Light Snack and Soft
Drinks Provided.

The "Hidden Schematic" in High Performance Circuits

As processor speeds continue to increase, signal integrity and especially impedance control, has become a big part of EMI design in printed circuit boards. The textbook tells you what to do, but doesn't adequately discuss things that usually go wrong. We call these parasitic factors the "Hidden Schematic" - the stray inductance, capacitance and impedance discontinuities that prevent the circuit from performing as intended as well as increasing emissions. This paper discusses the hidden schematic - the parameters that don't show up in the real schematic, and how to cope with them.

Speaker Biography: William Kimmel

William Kimmel is a principal in the consulting firm of Kimmel Gerke Associates, Ltd., specializing in electromagnetic compatibility (EMC). He has over 40 years experience in EMC design and troubleshooting in military, automotive, industrial, medical, and commercial applications.

He has a BSEE from the University of Minnesota, is a Registered Professional Engineer, a NARTE Certified EMC and ESD Engineer, and an active Member of the IEEE EMC Society. He publishes and lectures widely on various EMC topics.

Meeting Location

