



**EDS Mini-Colloquium
On
New Frontiers of Compact Modeling**
October 10, 2008
Santa Clara University, Santa Clara, CA

Sponsored by
IEEE Electron Devices Society

9:00 AM: Registration

9:20 AM: Introduction: Dr. S.K. Saha, Chair, *IEEE EDS Compact Modeling Technical Committee, Silterra USA Inc., San Jose, CA.*

9:20 AM: Welcome Address, Prof. C. Yang, *Chair, Electrical Engineering Department, Santa Clara University, Santa Clara, CA.*

9:30 AM – 12:00 Noon: Session I

Chair: Prof. T. Yamada, *Electrical Engineering Department, Santa Clara University, Santa Clara, CA.*

9:30 AM: Compact modeling of silicon controlled rectifier for electrostatic discharge (ESD) computer-aided design applications, Prof. J.J. Liou, *School of Electrical Engineering and Computer Science, University of Central Florida, Orlando, Florida.*

10:20 AM: Noise issues in advanced silicon devices and circuits, Prof. M.J. Deen, *Electrical and Computer Engineering Department, McMaster University, Hamilton, ON, Canada.*

11:10 AM: Modeling and characterization of RF/analog and noise using HiSIM2, Prof. N. Sadachika, *Hiroshima University, Hiroshima, Japan.*

12:00 PM – 2:00 PM: **Lunch Break & Student Poster Session**

2:00 PM – 5:00 PM: Session II

Chair: Dr. J. Prasad, *DSM Solutions Inc., Los Gatos, CA.*

2:00 PM: Compact modeling of multiple-gate MOSFETs, Prof. Y. Taur, J. Song, and B. Yu (speaker), *Department of Electrical and Computer Engineering, University of California – San Diego, La Jolla, CA.*

2:50 PM: Nanoscale MOSFET physics: Observations from non-compact modeling studies, Prof. L.F. Register, *Department of Electrical and Computer Engineering, and the Microelectronics Research Center University of Texas, Austin, TX.*

3:40 PM: Wafer bonding for heterogeneous integration, Prof. P.K. Yu, *Department of Electrical and Computer Engineering, University of California – San Diego, La Jolla, CA.*

4:30 PM: Closing Remarks: Dr. S.K. Saha, *IEEE EDS Compact Modeling Technical Committee, Silterra USA Inc., San Jose, CA.*