Chapters

Argentina
Colombia
Guadalajara
Mexico
Peru
Puebla
Puerto Rico
São Paulo
Rio de Janeiro
Rio Grande do Sul
Venezuela

+4 possible new chapters in 2011
face-to-face meetings & teleconference meetings

also a large use of email discussions between chapter chairs

each chapter have to do an annual report
LASCAS
IEEE Latin American Symposium on Circuits and Systems
Each year organized by a different Chapter
Flagship Conference of CASS in R9

CASS Summer Schools
Each year dedicated to a different state-of-the-art subject
Each year organized by a different Chapter

SBCCI
Symposium on Integrated Circuits and Systems Design
First edition in 1983
LASCAS 2010
First IEEE Latin American Symposium on Circuits and Systems
February 24-26, 2010  Iguassu Falls, Brazil-Argentina-Paraguay

LASCAS 2011
Second IEEE Latin American Symposium on Circuits and Systems
February 23-25, 2010  Bogotá, Colombia

LASCAS 2012
Second IEEE Latin American Symposium on Circuits and Systems
February 29 - March 2, 2012  Playa del Carmen, Mexico
LASCAS 2011
Second IEEE Latin American Symposium on Circuits and Systems
February 23-25, 2011   Bogotá, Colombia
webpage: lascas.org
LASCAS’ goal is to be the International Symposium of IEEE Circuits and Systems in Latin America. Its third edition will take place in Playa del Carmen. Originally named Xaman-Ha (waters of the north) and known as the capital of the Riviera Maya, Playa del Carmen is a living vibrant part of the Maya World (Mundo Maya). Located on the beautiful Caribbean coast some 40 miles (60 kms.) south of Cancun, Playacar, as it is now affectionately known, is the perfect base from which to explore the fascinating sites along the Cancun-Tulum Corridor of Quintana Roo (now called Mayan Riviera).

The symposium will cover technical novelties and tutorial overviews on circuits and systems topics including, but not limited to:

- Analog and Digital Signal Processing
- Biomedical Circuits and Systems
- Multimedia Systems and Applications
- Nanoelectronics and Gigascale Systems
- Cellular Neural Networks and Array Computing
- Neural Systems and Applications
- Circuits and Systems for
- Communications Nonlinear Circuits and Systems
- Computer Aided Design
- Power Systems and Power Electronic Circuits
- Sensory Systems
- Graph Theory and Computing
- Visual Signal Processing and Communications
- Life Science Systems and Applications
- VLSI Systems and Applications
- Electronic Testing
- Fault Tolerant Circuits

General Chairs:
Arturo Sarmiento, INAOE, Mexico
Ricardo Reis, UFRGS, Brazil

CALL FOR PAPERS
Paper Submission: November 14, 2011
Special Session Proposal: November 14, 2011
Notification of acceptance: December 16, 2011
Camera-ready: January 14, 2012

http://www.elec.inaoep.mx/~LASCAS12
1st IEEE CASS Summer School
Physical Design of Reliable Circuits
January 17-21, 2011, Porto Alegre, Brazil

2nd IEEE CASS Summer School
Pushing the Boundaries of Energy Efficiency in Low Power Design
January 17-21, 2011, Cusco, Peru

3rd IEEE CASS Summer School
Design of 3D circuits
February 6-10, 2012, Buenos Aires, Argentina
First IEEE CASS Summer School
Physical Design of Reliable Circuits

January 12-15, 2010
Porto Alegre, Brazil
Program

Tuesday, January 12:
1- Noise and Aging Effects [Gilson Wirth/Roberto Silva- UFRGS]
2- Atomistic Simulations on Reliability [Dragica Vasileska/Arizona State University, USA]
3- Radiation Effects and Tolerance Techniques [Ricardo Reis/Fernanda Kastensmidt-UFRGS]

Wednesday, January 13:
4- Compact Modeling of CMOS Variability and Reliability [Yu Cao/Arizona State University, USA]
5- On-chip Characterization of Circuit Reliability [Chris Kim/Univ. Minnesota, USA]
6- Panel: Emerging Reliability Threats

Thursday, January 14:
7- Circuit Layout for Reliability [Frank Liu/IBM Austin Research Lab, USA]
8- Variability Aware Clock Design [Matthew Guthaus, UCSC, USA / Gustavo Wilke, UFRGS]

Friday, January 15:
9- Leakage Power Minimization and Thermal Effects Compensation [Enrico Macii/Polito, Italy]
10- Circuit Resilience Roadmap [Kevin Nowka/IBM Austin Research Lab, USA]
2nd IEEE CASS Summer School
Pushing the Boundaries of Energy Efficiency in Low Power Design

January 17-21, 2011

Cusco, Peru
Monday, January 17:
1- Energy-Efficient On-Chip Networks - Vijay Narayanan, Pennsylvania State University, USA

Tuesday, January 18:
2- Cooling Techniques for 3D integrated Circuits - Maciej Ogorzalek
Jagiellonian University Krakow, Poland
3- Reducing Power Consumption by using Network of Transistors - Ricardo Reis, UFRGS, Brazil

Wednesday, January 19:
4- Ultra-low Power VLSI Circuits: Voltage Limits and Design Techniques - Massimo Alioto
Univ. Siena, Italy and Berkeley Wireless Research Center, USA
5- Energy Efficient Enterprise Computing Systems, Massoud Pedram, U. Southern Ca., USA
6- Research Panel

Thursday, January 20:
7- BIST feasible in Low-Power Heterogeneous Integrated Systems, Jose Luis Huertas, CNM, Spain
8- Low-voltage and Error Resilient Design - Kaushik Roy, Purdue University, USA

Friday, January 21:
9- Low-Power Design of Mixed-Signal Interface Circuits, Angel Rodriguez-Vasquez, CNM, Spain
10 - CASS Presentation
11- Cultural Talk: INCAS Engineering

Saturday, January 22:
Optional Tour to Machu Pichu
3rd IEEE CASS Summer School
3D Integrated Circuits Technologies

February 6-9, 2012
Buenos Aires, Argentina
SBCCI2011
24th SYMPOSIUM ON INTEGRATED CIRCUITS AND SYSTEMS DESIGN
CHIP ON THE CLIFFS
www.sbc.org.br/sbccci

João Pessoa, BRAZIL
August 30 to September 2, 2011

Estação Cabo Branco - Ciência, Cultura & Artes
CHIP ON THE CLIFFS
SBCCI2011
SBMicro2011
August 30 - September 2, 2011
João Pessoa, Brazil
Circuits and Systems Society  Region 9

Some Regular Chapter Activities

EAMTA
Argentine School of Micro and Nanoelectronics, Technology and Applications - Chapter Argentina

EMicro
Microelectronics School - Chapter Rio Grande do Sul, Brazil

SIM
South Symposium on Microelectronics - Chapter Rio Grande do Sul, Brazil
INTERCON
co-sponsored by Chapter Peru

Microelectronics School
Chapter Mexico City, Mexico

CASME
Circuits and Systems for Medical and Environment Applications
Mérida - Mexico, Chapter Puebla
Some Regular Chapter Activities

CWCASS
Colombian Workshop on Circuits and Systems
Chapter Colombia

ANDESCON
conference of the IEEE Andean Council
co-sponsored by CASS Chapter
2010 co-sponsored by Chapter Colombia
The IBERCHIP Workshop provides an annual forum for academic and industrial researchers from Iberoamerican countries in which to exchange experiences, share knowledge and establish relations to foster the development of activities related to the field of Microelectronics. Special emphasis is placed on the improvement of education and training, and in the promotion of joint cooperative projects. This year, the 18th edition of the Workshop will be held in Playa del Carmen, Mexico. It is located on the northeast coast of the Yucatan peninsula, Quintana Roo.

Topics of Interest include, but are not limited to:
- Analog and Mixed Signal IC Design
- Applications of High Level Description Languages
- High Level Logic Synthesis
- Digital Design
- Programmable Devices and Rapid Prototyping
- Test of Integrated Circuits
- Sensors and Microsystems
- Nanotechnology
- IP Blocks and Design for Reusability
- Device Modelling
- Hardware/Software Codesign
- Fuzzy Logic and Neural Networks
- Embedded Systems
- Design and Synthesis for Testability
- System on Chip Design
- RF Circuits
- Biomedical systems
- MEMS
- Digital Signal Processing
- Industrial Applications
- Power Electronics
- Low Power / Low Voltage Design
- Design Automation Tools
- Education
- Other Microelectronics related topics

General Chair:
José Luis Huertas, IMS CNM, España
Roberto Murphy, INAOE, Mexico
http://www-elec.inaoep.mx/~WS2012

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Edgar Sánchez-Sinencio

Edgar Sánchez-Sinencio (F’92) was born in Mexico City, Mexico. He received the degree in communications and electronic engineering (Professional degree) from the National Polytechnic Institute of Mexico, Mexico City, the M.S.E.E. degree from Stanford University, CA, and the Ph.D. degree from the University of Illinois at Champaign-Urbana, in 1966, 1970, and 1973, respectively. In 1974 he held an industrial Post-Doctoral position with the Central Research Laboratories, Nippon Electric Company, Ltd., Kawasaki, Japan. From 1976 to 1983 he was the Head of the Department of Electronics at the Instituto Nacional de Astrofísica, Optica y Electrónica (INAOE), Puebla, Mexico. He is currently the T.J. Kilby Chair Professor and Director of the Analog and Mixed-Signal Center at Texas A&M University. He was the General Chairman of the 1983 26th Midwest Symposium on Circuits and Systems. He was an Associate Editor for IEEE Trans. on Circuits and Systems, (1985-1987), and an Associate Editor for the IEEE Trans. on Neural Networks. He is the former Editor-in-Chief of the Transactions on Circuits and Systems II. He is co-author of the book “Switched Capacitor Circuits” (Van Nostrand-Reinhold 1984), and co-editor of the book “Low Voltage/Low-Power Integrated Circuits and Systems” (IEEE Press 1999), co-author of the book “CMOS PLL Synthesizers: Analysis and Design” (Springer 2005). He has published about 150 journal papers and 250 conference papers, he has more than 1200 citations in the Scientific Citation Index. He has graduated nearly 30 Ph.D. students. In November 1995 he was awarded an Honoris Causa Doctorate by the National Institute for Astrophysics, Optics and Electronics, Mexico. The first honorary degree awarded for Microelectronic Circuit Design contributions. He is co-recipient of the 1995 Guillmin-Cauer for his work on Cellular Networks. He is a former IEEE CAS Vice President-Publications. He was also the co-recipient of the 1997 Darlington Award for his work on high-frequency filters. He received the Circuits and Systems Society Golden Jubilee Medal in 1999. He was the IEEE Circuits and Systems Society, Representative to the Solid-State Circuits Society (2000-2002). He is a former member of the IEEE Solid-State Circuit Fellow Award Committee. His present interests are in the area of Medical Electronics, RF-Communication circuits and low voltage analog and mixed-mode circuit design. He is an IEEE Fellow Member since 1992.
CASS DLP Program

several talks in several chapters

From 20 DLP speakers in 2011, 3 are from Region 9
Student Grants

Some grants are dedicated to support CASS student members to participate in CASS events organized in the region.
World Chapter of The Year Award
Peru got the CASS World Chapter of The Year Award 2010

Regional Chapter of The Year Award
Puebla got the CASS R9 Chapter of The Year Award 2010
Colombia got the CASS R9 Chapter of The Year Award 2010
New Logo

Done by a designer from Brazil contacted by Chapter Rio Grande do Sul
Enjoy ISCAS in Rio
Rio - May 15th to 18th 2011
www.iscas2011.org