Reliability / CPMT / ED Singapore Chapter

IEEE EDS Distinguished Lecturer Talk
Organized by IEEE Singapore Rel/CPMT/ED Chapter and co-hosted by Nanyang Technological University

"The IC Industry in China: Challenges and Opportunities"

Speaker : **Professor Mansun Chan**
Department of ECE
Hong Kong University of Science & Technology, HK

Date : **27 June 2007, Wednesday**
Time : 4pm – 5pm
Venue : **Executive Seminar Room**, Block S2.2 (S2.2-B2-53)
Map : http://www.street-directory.com/ntu/campus.cgi?no=School+of+Electrical+and+Electronic+Engineering+%28EEE%2C+Block+S2.2%29&map_search=nanyang&search.x=19&search.y=5

Admission : Free (no pre-registration required)

ABSTRACT

The global IC industry has undergone tremendous changes in the past ten years. Traditionally, the IC design industry is dominated by big companies in the North America. The globalization of the economy has generally made the labor cost to do IC design in the North America very high. With the availability of high end foundries in the Asia area, the IC design industry in the region is booming, taking the advantages of lower local wages and more returnee from North America bringing in their expertise in IC design. Among the Asian countries, China shows very high potential to be one of the leaders with the supply of low cost local talents, large domestic market and underdeveloped economy. However, doing IC design business in China also involve risks and challenges significantly different that in more developed countries. With proper understanding, planning, investment, and implementation, there is a good chance that a successful business in IC design can be created. With the experience of startup IC design houses in China, this talk intent to describe and analyze the possible implementation of a successful IC design business in China. Possible approaches and plans that could take advantage of the local resources to propel the IC business in China will also be discussed.

ABOUT THE SPEAKER

Dr. Mansun Chan completed his MS degree in 1994 and Ph.D degree in 1995 at University of California at Berkeley. His research at Berkeley covered a broad area in silicon devices ranging from process development to device design, characterization, and modeling. A major part of his work was on the development of record breaking Silicon-On-Insulator (SOI) technologies. Dr. Chan has also maintained a strong interest in device modeling and circuit simulation. He is one of the major contributors to the unified BSIM model for SPICE, which has been accepted by most US companies and the Compact Model Council (CMC) as the first industrial standard MOSFET model. In January 1996, he has joined the EEE faculty at Hong Kong University of Science and Technology. His research interests include nano-device technologies, image sensors, SOI technologies, high performance IC, 3D Circuit Technology, device modeling and Nano BIOMEMS technology. Starting in 2000, Dr. Chan has started to get involved in starting business in China and founded the first consulting company. Since then, he has helped to co-found more than 10 IC design houses in China. Currently, he is in charge of the business strategy of one company and consulting with many startup IC design houses in China.