

The 13th WIMNACT - Hong Kong/Singapore

Workshop and IEEE EDS Mini-colloquium on Nanometer CMOS Technology

Co-sponsored by IEEE EDS Distinguished Lecturer Program

23 July 2007: Organized and co-sponsored by IEEE ED/SSC Hong Kong Chapter

25 July 2007: Organized and co-sponsored by IEEE Rel/CPMT/ED Singapore Chapter

Co-hosted by Microelectronics Centre, School of EEE, Nanyang Technological University

Dates/Venues: 23 July 2007 (Monday), Hong Kong
25 July 2007 (Wednesday), Singapore

Program – Singapore: 25 July 2007, 8:30am – 5:30pm

Venue: Lecture Theatre 25 (LT 25), South Spine, Nanyang Technological University

- 8:30 – 8:45 **REGISTRATION**
- 8:45 – 9:00 **Welcome Address by IEEE Singapore Rel/CPMT/ED Chapter Chair**
Dr. Alastair Trigg, Institute of Microelectronics, Singapore
- 9:00 – 9:10 **Introduction to EEE Microelectronics Centre by MEC Director**
Prof. Kin-Leong Pey, Nanyang Technological University, Singapore
- 9:10 – 10:00 **On-Chip Spiral Inductors in CMOS Technology for RF Applications**
Prof. Juin J. Liou, University of Central Florida, USA
- 10:00 – 10:20 **BREAK (catered by organizer)**
- 10:20 – 11:10 **Physics-Based Models for Performance Evaluation of a Nanoscale MOSFET**
Prof. Vijay K. Arora, Wilkes University, USA; Universiti Teknologi Malaysia, Malaysia
- 11:10 – 12:00 **Silicided Hyper-shallow p^+/n^- Junctions Formed by Pulsed Laser Annealing for Nanoelectronic Devices**
Prof. Kin-Leong Pey, Nanyang Technological University, Singapore
- 12:00 – 1:30 **LUNCH**
- 1:30 – 2:20 **Device Design and Optimization Challenges for Nano-scale Multi-gate MOSFETs**
Prof. V. Ramgopal Rao, Indian Institute of Technology, India
- 2:20 – 3:10 **Unified Compact Modeling of Emerging Multiple-Gate MOSFETs**
Prof. Xing Zhou, Nanyang Technological University, Singapore
- 3:10 – 3:30 **BREAK (catered by organizer)**
- 3:30 – 4:20 **Application of Integrated Circuit Technology for Biological Material Analysis**
Prof. Mansun Chan, Hong Kong University of Science & Technology, Hong Kong
- 4:20 – 5:10 **Advanced Transmission Electron Microscopy for Nano-electronics Device and Process Analysis**
Mr. Chih-Hang Tung, Institute of Microelectronics, Singapore
- 5:10 – 5:30 **CLOSING REMARKS & PRESENTATION OF TOKENS OF APPRECIATION**

Related websites:

WIMNACT <http://www.ntu.edu.sg/eee/eee6/conf/WIMNACT07.htm>
Rel/CPMT/ED <http://ewh.ieee.org/soc/cpmt/singapore/>
IEEE/EDS <http://www.ieee.org/portal/pages/society/eds/>