

## 2018 IEEE International Memory Workshop

May 13<sup>th</sup> – 16<sup>th</sup>, 2018 Westin Miyako, Kyoto, Japan

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- **Submission deadline **February 1<sup>st</sup> 2018****
- **Single submission of final, up to four-page paper**
- **Notification of acceptance early March**
- **Invited talks given by reknown experts**
- **Papers solicited in all aspects of semiconductor memory technology (Flash, DRAM, PCRAM, RRAM, MRAM, SRAM, embedded memory, system, and other NV memories)**
- **Evening panel discussion**
- **Tutorial on Sunday 13<sup>th</sup>**

### CALL FOR PAPERS

The 10<sup>th</sup> IEEE International Memory Workshop (IMW) will be held at the Westin Miyako Kyoto, Japan from Sunday May 13<sup>th</sup> though Wednesday May 16<sup>th</sup>, 2018. It is sponsored by the IEEE Electron Devices Society and meets annually in May. The IMW is a unique forum for specialists in all aspects of memory microelectronics and people with different backgrounds who wish to gain a better understanding of the field. The morning and afternoon technical sessions are organized in a manner to provide ample time for informal exchanges amongst presenters and attendees. The evening panel discussions will address hot topics in the memory field.

Papers are solicited in all aspects of semiconductor memory technology (Flash, DRAM, SRAM, PCRAM, RRAM, MRAM, embedded memory, system, and other NV memories), including but are not limited to:

- **Device Physics / Cell Design & Scaling System Architecture**
- **Error Management & Algorithms**
- **New Concepts & Disruptive Technologies**
- **Quality & Reliability Circuit Design & Algorithms**
- **SSD / Modeling / Emerging Applications & Markets**

### PAPER SUBMISSION

Please submit an electronic abstract in Adobe Acrobat (preferred) or Microsoft Word.

The suggested format can be found at: [http://www.ieee.org/documents/MSW\\_USltr\\_format.doc](http://www.ieee.org/documents/MSW_USltr_format.doc). Submit your abstract on the IEEE IMW website (<http://www.ewh.ieee.org/soc/eds/imw/>).

The abstract should be no more than four pages including supporting figures and tables.

**The deadline for submitting abstracts is February 1<sup>st</sup>, 2018.**

The proceeding with all the accepted abstracts will be provided to the participants at the workshop as a bound book and in electronic format. It will also be submitted to the IEEE for submission to the Xplore database for easy referencing. In keeping with the workshop nature of the meeting, no photograph or recording will be allowed. Submissions from universities are encouraged.

There is a subsidized registration fee for students and IEEE Members. The maximum attendance at the workshop is limited. Therefore, advance registration is highly recommended.

General Chair	Technical Chair	Finance Chair	Publicity Chair
<b>Gabriel Molas</b>	<b>Akira Goda</b>	<b>Wei Zhiqiang</b>	<b>Randy Koval</b>
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