



**Invitation to Attend  
the combined ....**



IEEE COMPONENTS, PACKAGING AND  
MANUFACTURING TECHNOLOGY SOCIETY

**International Symposium on  
Advanced Packaging Materials (APM 2007) and the  
Int'l Electronics Manufacturing Technology Symposium  
October 3-5, 2007 Holiday Inn San Jose/Silicon Valley, CA USA**

The **Advanced Packaging Materials Symposium** (APM 2007) is an international premier technical event on electronic packaging materials organized by the IEEE's CPMT Society and its Santa Clara Valley Chapter. APM 2007 features Keynote talks, technical presentations, and exhibits. The Symposium provides leading-edge coverage of developments in all areas of packaging materials and processes. Attendees in the past have included academic researchers, developers, producers, and users of packaging materials from all over the world. APM is also a major packaging materials forum, providing opportunities to network and meet leading experts and exchange up-to-date packaging knowledge in the field.

The 32nd **International Electronics Manufacturing Technology** (IEMT) Conference is the premier IEEE event devoted to the manufacture of electronic, optoelectronic and MEMS/sensors devices and systems. IEMT is an established International conference of long standing organized by the CPMT Society. Through well-mediated technical papers IEMT offers manufacturing technologists as well as designers a single convenient forum to assess new packaging and assembly technologies about to enter production or in production around the world. In addition, IEMT also provides attendees the opportunity to meet leading domain experts and network with colleagues.

**Exhibition**

A tabletop exhibition featuring suppliers of materials, equipment, components, software, and services to the electronics industry will also be held at the venue of the symposium. Please contact Annette Teng for more details about exhibiting: [Annette@corwil.com](mailto:Annette@corwil.com)

Dr. Srinivas Rao, Flextronics, General Chair, IEMT  
Dr. Dongkai Shangguan, Flextronics, Gen Chair, APM  
Choong Kooi Chee, Intel, Vice General Chair, IEMT  
Dr. KRS Murthy, Program Chair, IEMT  
Dr. Paul Wang, Microsoft, Program Chair, APM  
Azhar Aripin, OnSemi, Vice Program Chair, IEMT  
Paul Wesling, Administration Chair, IEMT & APM

Welcome to Silicon Valley and "California Casual," as we gather in the atriums, patios, pool, fitness center and session rooms of the Holiday Inn San Jose. Our venue is a Mediterranean resort-style hotel nestled in a beautifully landscaped 17 acres in the heart of Silicon Valley. So near Silicon Valley's action – so relaxing and informal, for our focus on technical interchange. And you are only steps from the Santa Clara Valley's Light Rail line, for quickly heading downtown or to other destinations. We are minutes (1.6 km / 1 mile) from San Jose International Airport, with complimentary shuttle to/from the hotel.

**Professional Development Courses:**

Wednesday Oct. 3 is devoted to morning and afternoon PDCs to allow you to quickly master new skills and disciplines that will be useful as you tackle current challenges or new opportunities.

**Sessions on:**

- Materials for Electronics
- Nano Materials, Nano-Toxicology
- Lead-Free Experiences and Challenges
- Design for Environment/Recycling
- Embedded Passives
- Manufacturing Technology
- Testing
- Packaging Integration Applications (SiP, WLP, etc)
- MEMS and Sensors Manufacturing
- Display and Opto Manufacturing
- Materials for Packaging Applications
- Poster Papers

**Register today! Attendance is limited by the nature of the facilities, to allow better sharing and interaction.**

**Please visit our websites:**

[www.cpmt.org/iemt](http://www.cpmt.org/iemt) or [www.cpmt.org/apm](http://www.cpmt.org/apm)

One admission allows entry to sessions across both Symposia