

ESTC 2008

2nd Electronics System-Integration Technology Conference
1st – 4th September 2008, Greenwich, London, UK

Call for papers

Authors from industry and academia with innovative research or developments are invited to submit Abstracts for this forthcoming major international Conference. Papers from industry are especially important. Such papers will necessarily have product content. However, commercially oriented material and descriptions are not permitted either in the paper or in the presentation or poster. Authors will also benefit from the opportunity for papers to be selected from the Conference Proceedings for publication in IEEE CPMT themed journals. Poster papers will have specific scheduled presentation slots. Proposals for short-course Master Classes are also invited.

Themed topics of ESTC 2008 include:

- **Advanced Packaging**
- **Modeling, Simulation and Design**
- **Emerging Technologies**
- **Manufacturing Technology**
- **Assembly of Photovoltaics**
- **New Materials and Processes**
- **Power Electronics**
- **Reliability for Micro and Nano Systems**
- **Optoelectronics**

Special Sessions with invited speakers:

- **Green Electronics** • **Standards**
- **Prognostics and Health Management**
- **Photovoltaics – Asia Pacific**
- **European Business Global Council**

Call for Exhibitors

The ESTC 2008 exhibition will be based on a 3m x 3m shell scheme in an exhibit area which will accommodate more than 40 stands. Early bookings are strongly advised. Early bookers will receive a significant discount on the normal stand price as follows: The full stand price is £1800. A 20% Early Eagle discount is available for bookings made and paid for before **30th September 2007** and a 10% Early Bird discount is available for bookings made before **29th February 2008**.



General Chair Prof. Chris Bailey, University of Greenwich chris.bailey@estc.biz

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The 2nd ESTC **Electronics Systems-Integration Technology Conference** (ESTC 2008) follows the very successful first ESTC held in Dresden in 2006. ESTC 2006 comprised over 200 technical papers, outstanding Invited Speakers and an attendance exceeding 400 from across the globe, supplemented by 6 short courses with over 50 attendees. A major technical exhibition was held alongside the Conference - an excellent opportunity for suppliers of materials, design houses, circuit manufacturers to promote their products and reach potential customers.

ESTC 2008 will be held at Greenwich, a World heritage site and major maritime site on the banks of the River Thames in the magnificent city that is London. The Conference and Exhibition will be hosted by the University of Greenwich in the buildings of the former Royal Naval College. Plenary sessions will be in the Great Painted Hall - providing a wonderful ambience for the technical presentations. The exhibition will accommodate more than 40 stands and aims to be the quality event in microelectronics and micro-systems in Europe.



Abstract Guidelines

An abstract means abstracting a summary of the whole paper, presentation, or research project. This includes a summary of the purpose of the research or activity (i.e. what was the problem), followed by a summary of the method used to solve the problem and finally a summary analyses and conclusions including summarising actual data. The skill of an author is in being able to convey summary information concisely and informatively. The adjunction process may remove authors' names and affiliations. So, well known authors as well as new authors are urged to respect the request to submit a full and true abstract.

- **Deadline for receiving Abstracts is 31st January 2008**
- Successful and Unsuccessful authors will be notified no later than 31st March 2008
- Publication-ready full papers are required no later than 31st May 2008 for final adjunction on compliance with the approved Abstract and technical requirements of the Conference.

More information: www.estc.biz